

Basic Faculty Information

Full Name: Prof. Pranjali Shrivastava

Designation: Assistant Professor | Department: CSE & AIDSE | Date of Joining: 25 Aug 2025 | Official Email ID: pranjali@shrivastava.shriram@gmail.com | Contact Number: 9399561073



Recent Passport Size Photograph (HD)

Professional Qualification: PHD (Pursuing) in Cyber security | MTECH in IOT

Teaching Experience: 1 year

Total Teaching Experience (Years): 1 year

Industry Experience (if any): 3 years Exp Software Engineer in Infosys Ltd Bengaluru

Areas of Specialization: IOT

Subjects Taught with Ideology approaches in Real World problem domain: Cloud computing, IoT, Real time operating system

Research and Publications: Joined International conferences and completed AI based FDP Program

Number of Research Papers Published

Journals / Conferences / Book ISBN Details

Research Identity Links

Google Scholar / ORCID / Scopus ID / DOI (if available):

Kindly provide authenticated public web-links for identity verification (not private links).

Technical Skills: Python, Mainframe, DB2 and IoT lab

Professional Memberships

IEEE / ACM / CSI / ISTE (with your membership ID and links)

Technical Activities Conducted

Activity Title: One day workshop on Internet of Things | Date: 9 Dec 2025 | Venue: Central Lab

Description: Our One Day workshop on Internet of things (IoT) empowered students with practical insight, real-world applications and smart devices to connected systems and industry-relevant skills. Heartfelt thanks to our expert speaker Prof. Pranjal Shrivastava for an engaging and informative session, and to Prof. Deepti Gupta for seamless coordination.

SRIIT SHIRAM INSTITUTE OF INFORMATION TECHNOLOGY
National Expressway, A.B. Road, Banmore Near Gwalior (M.P.)

One Day
Workshop
on
Internet of Things

Date - 9th December 2025
Time - 11:00 AM to 01:00 PM
Venue - Central Lab, SRIIT

Speaker:
Prof. Pranjal Shrivastava

Audience :- B.Tech 2nd and 3rd Year CSE/AIDSE

Call for Details
☎ 9109035202, 9755265202
🌐 www.shriraminstitute.ac.in

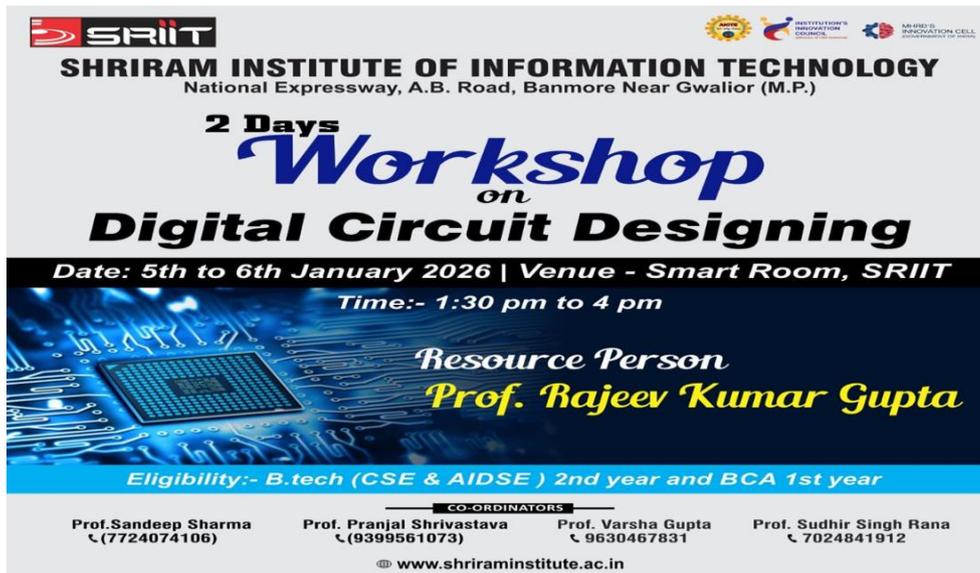
Coordinator -
Prof. Deepti Gupta

Photos:



Activity Title: Two day workshop on Digital circuit Design | Date: 5-6 Jan 2026 |
Venue: Smart Room

Description: A Workshop on Digital circuit design was organized by Dept. of CSE and EC SRIT B.Tech (IIInd year) and BCA 1st year Prof. Rajeev Kumar Gupta (Assistant Professor Deptt. Of EC) has given a Two day workshop on Digital circuit design. He explained various important topics related to Circuit design and covering fundamental like logic gates, and PCB layout.



The poster is for a 2-day workshop on Digital Circuit Designing. It features the SRIT logo at the top left and several institutional logos at the top right. The main title is "2 Days Workshop on Digital Circuit Designing" in a large, stylized font. Below the title, it specifies the date as "Date: 5th to 6th January 2026" and the venue as "Venue - Smart Room, SRIT". The time is listed as "Time:- 1:30 pm to 4 pm". The resource person is identified as "Prof. Rajeev Kumar Gupta". The eligibility is "Eligibility:- B.tech (CSE & AIDSE) 2nd year and BCA 1st year". At the bottom, there are four coordinators: Prof. Sandeep Sharma, Prof. Pranjal Shrivastava, Prof. Varsha Gupta, and Prof. Sudhir Singh Rana, each with a contact number. The website "www.shriraminstitute.ac.in" is also provided.

Photo:



Technical Seminars Delivered

Seminar Title | Date | Venue | Target Audience | Brief Description | Photos

Technical Debate / Panel Discussion

Topic | Date | Venue | Participants | Summary | Photos

Awards / Achievements: Completed Faculty Development Program



SRIT ShriRam Institute of Information Technology
National Expressway, A.B. Road, Banmore Near Gwalior (M.P.)

Certificate

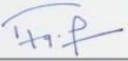
OF PRESENTATION

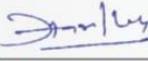
This Certificate is proudly presented to

Mr. Pranjali Shrivastava has successfully presented a research paper titled "Detection of emerging Cyber Attacks in IOT network using Deep learning" at the 1st International Conference on Artificial General Intelligence for a Smarter, Safer and Sustainable Future: Global Synergies India's Roadmap to Viksit Bharat @ 2047 organized by Department of Commerce and Management, ShriRam Institute of Information Technology, in association with Jiwaji University, on November 22, 2025.

We appreciate their valuable contribution towards knowledge exchange and innovation.


Dr. Amitabha Maheshwari
Convener of Conference, HOD
Dept. Of Commerce & Management


Dr. Pavan Agrawal
Patron of Conference
Director SRIT


Shri Harendra Sharma
Chief Patron,
& Secretary SRGOC



Faculty Social / Professional Links (if any)